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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/943,763	08/30/2001	David J. Corisis	3388.6US (97-560 .5)	2592

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TRASK BRITT
P.O. BOX 2550
SALT LAKE CITY, UT 84110

EXAMINER

MITCHELL, JAMES M

ART UNIT	PAPER NUMBER
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2827

DATE MAILED: 04/14/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/943,763	CORISIS, DAVID J.
	Examiner James Mitchell	Art Unit 2827

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 07 January 2002.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

4) Claim(s) 1-3 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1-3 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
 If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
 a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____ .
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ .	6) <input type="checkbox"/> Other: _____ .

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ueda et al. (US 5,334,872) in combination Golwalkar et al. (US 5,527,740).

Ucheda (Fig 2, 10) discloses an assembly method for a semiconductor device assembly using a wirebonding (37) by inherently attaching at least two bond wires to bond pads (not labeled) of semiconductor device (35a) and portions of the inner leads (23) and forming a strip lead frames (21), said strip having opposed rails (21), having dam bars (not labeled) between said opposed rails, having at least two inner (23) and outer (24) leads, having a die (35a), mount paddle (25) having at least one integral clamping tab (26) extending outwardly and having an upper surface below an upper level of said at least two inner leads, and inherently attaching a semiconductor to said die mount paddle, said device having a plurality of bond pads (not labeled)

Ueda does not appear to disclose aligning strip lead frames on a lower clamp member of said wire bonding device having an upper clamp member overlying portions of the at least two inner leads, outer leads and clamping tab, and removing strip of the lead frames and said semiconductor device from said lower clamp member and

encapsulating a portion of said strip of lead frames and said semiconductor device in a material.

However, Golwalkar (Fig 5b) utilizes aligning strip lead frames on a lower clamp member (118) of said wire bonding device having an upper clamp member (110), and removing strip of the lead frames and said semiconductor device from said lower clamp member and encapsulating a portion of said strip of lead frames and said semiconductor device in a material (Fig 9).

It would have been obvious to one of ordinary skill in the art to incorporate aligning strip lead frames of Ueda on a lower clamp member of said wire bonding device having an upper clamp member, such that the upper clamp member overlies portions of the at least two inner leads, outer leads and clamping tab that are inherently deformed in order to reduce vibration of the paddle or ("pad") during wire bonding as taught by Golwalkar (Column 5, Lines 60-66), and to remove strip of the lead frames and said semiconductor device from said lower clamp member and encapsulating a portion of said strip of lead frames and said semiconductor device in a material in order encapsulate the semiconductor device as required by Ueda (Fig 10, item 39).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Mitchell whose telephone number is (703) 305-0244. The examiner can normally be reached on M-F 10:30-8:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David L. Talbott can be reached on (703) 305-9883. The fax phone

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numbers for the organization where this application or proceeding is assigned are (703) 305-3432 for regular communications and (703) 305-3230 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.



JMM
April 11, 2003



DAVID E. GRAYBILL
PRIMARY EXAMINER